

AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph beginning at page 2, line 2, as follows:

- (c) Build models of each of the blocks (eVCs) in the SoC using an HVL (hardware verification language) such asa Specman to replace existing hardware blocks in turn, such that the modelled blocks can provide stimulus into the system. Here a problem is that the integrity of the system is compromised as existing blocks need to be removed and re-inserted per test.